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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/586,951	07/25/2006	Takeshi Ishigaki	SUZU1720	3389
44654 7590 01/16/2008 SPRINKLE IP LAW GROUP 1301 W. 25TH STREET			EXAMINER	
			ROJAS, DANIEL E	
SUITE 408 AUSTIN, TX	78705		ART UNIT	PAPER NUMBER
			4125	
			MAIL DATE	DELIVERY MODE
			01/16/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Application No. Applicant(s) 10/586,951 ISHIGAKI, TAKESHI Office Action Summary Examiner Art Unit DANIEL ROJAS 4125 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS. WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status 1) Responsive to communication(s) filed on 25 July 2006. 2a) This action is FINAL. 2b) This action is non-final. 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims 4) Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) _____ is/are allowed. 6) Claim(s) _____ is/are rejected 7) Claim(s) is/are objected to. 8) Claim(s) 1-20 are subject to restriction and/or election requirement. Application Papers 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are; a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. Priority under 35 U.S.C. § 119 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. Attachment(s) 1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)

Notice of Draftsperson's Patent Drawing Review (PTO-948)

Information Disclosure Statement(s) (FTO/S5/0E)
 Paper No(s)/Mail Date ________

Paper No(s)/Mail Date. ___

6) Other:

5) Notice of Informal Patent Application

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DETAILED ACTION

Election/Restrictions

Restriction is required under 35 U.S.C. 121 and 372.

This application contains the following inventions or groups of inventions which are not so linked as to form a single general inventive concept under PCT Rule 13.1.

In accordance with 37 CFR 1.499, applicant is required, in reply to this action, to elect a single invention to which the claims must be restricted.

Group I, claim(s) 1-5 and 20, drawn to a semiconductor integrated circuit device comprising a semiconductor substrate, counter, global reset circuit, local reset circuit, and a first circuit.

Group II, claim(s) 6-10, drawn to a semiconductor integrated circuit device comprising a semiconductor substrate having a first and second area, a plurality of counters in the first and second areas, a first clock circuit in the first area, a first circuit in the first area, a second clock circuit, and a second circuit in the second area.

Group III, claim(s) 11-19, drawn to a semiconductor integrated circuit device comprising a semiconductor substrate having a first area and a second area, a first counter in the second area, a first clock circuit, a first circuit in the second area, a first clock signal, and a second clock signal.

- 2. The inventions listed as Groups I-III do not relate to a single general inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, they lack the same or corresponding special technical features for the following reasons: the groups have a materially different design which would not be considered obvious variants to one of ordinary skill in the art. Groups II and III fail to teach the global and local reset of group I. Groups I and III fail to teach the first and second clock circuit provided on the first and
- second area, respectively, of group II and that the second circuit supplies the first circuit

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clock circuit), as described in group III.

with a second signal containing information on a value of the counter signal obtained upon reception of the first signal, as also described in group II. Groups I and II fail to teach a first counter and a first circuit in the second area and that the first clock circuit is not restricted to being on the first area (claim 11 does not specify a location for the first

3. Applicant is advised that the reply to this requirement to be complete must include (i) an election of a species or invention to be examined even though the requirement may be traversed (37 CFR 1.143) and (ii) identification of the claims encompassing the elected invention.

The election of an invention or species may be made with or without traverse. To preserve a right to petition, the election must be made with traverse. If the reply does not distinctly and specifically point out supposed errors in the restriction requirement, the election shall be treated as an election without traverse.

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANIEL ROJAS whose telephone number is (571)270-5070. The examiner can normally be reached on Monday-Friday 7:30-5 EST, alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Charles Garber can be reached on 571-272-2194. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/D. R./ Examiner, Art Unit 4125

/Charles D. Garber/ Supervisory Patent Examiner, Art Unit 4125